

POLYRESET

Polymer PTC Resettable Fuse Disk Type

CR series

(1) Features

1. Overcurrent and overtemperature protection device has a low resistance and high hold current.
2. Very low internal resistance.

(2) Applications

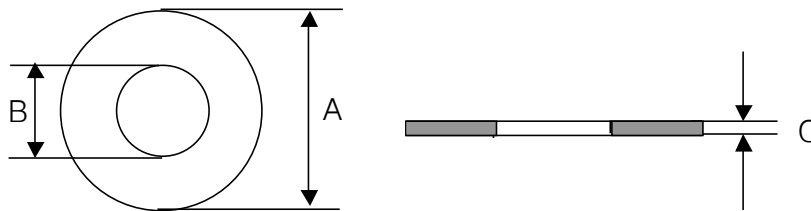
1. Battery protection.

(3) Ordering Information

PR - CR - 230 A - B
 (1) (2) (3) (4) (5)

- (1) Polyreset Product Designator
- (2) Product Characteristics ex : CR, SP, LS, LR, VS
- (3) Hold Current (×0.01 Amp)
- (4) Battery Type
A : CR123A, B: CR2
- (5) Packaging ex. B : Bulk

(4) Shape and Dimension



Unit : millimeters

Part number	A		B		C	
	min.	max.	min.	max.	min.	max.
PR-CR-230-A	14.35	14.45	6.25	6.35	0.26	0.36
PR-CR-230-B	12.90	13.10	3.95	4.05	0.26	0.36

(5) Specifications

◆ Electrical Characteristics

Part number	V _{max} (V)	I _{max} (A)	I _H (A)	I _T (A)	Max.time to trip(s) @5A	Initial resistance		Post trip resistance
						R _{min} (Ω)	R _{max} (Ω)	R _{1 max} (Ω)
PR-CR-230-A	15	40	2.3	5.0	80	0.015	0.032	0.060
PR-CR-230-B	15	40	2.3	5.0	80	0.015	0.032	0.060

(6) Environmental Characteristics

ITEM	REQUIREMENT	TEST CONDITION
Operating/Storage Temperature		-40°C to +85°C
Maximum Device Surface Temperature in Tripped state		125°C
Passive Aging	±10% typical resistance change	+75°C, 1000 hours
Humidity Aging	±10% typical resistance change	+85°C, 85% R.H. 7days
Vibration	No change	MIL-STD-883C, Condition A

(7) Test Procedures And Requirement

ITEM	REQUIREMENT	TEST CONDITION
Visual/Mech.	Per physical description	Verify dimensions and materials
Resistance	$R_{min} \leq R \leq R_{max}$	In still air @23°C
Time to Trip	$T \leq \text{max. time to trip(seconds)}$	At specified current, V _{max} , 23°C
Hold Current	No Trip	30min. at I _{hold}
Trip Cycle Test	No arching or burning	V _{max} , I _{max} , 100 cycles
Trip Endurance	No arching or burning	V _{max} , 48hours

(8) Terms and Description

- Hold current (I_H)** : maximum current at which the device will not trip at 20°C
- Trip current (I_T)** : minimum current at which the device will always trip at 20°C (2×I_H)
- Typical power dissipation (P_d)** :typical amount of power dissipation by the device when in tripped state in 20°C still air environment
- R_{min}** : Minimum device resistance at 20°C prior to tripping
- R_{max}** : Maximum device resistance at 20°C prior to tripping
- R_{1 max}** : Maximum device resistance at 20°C measured 1 hour post trip

(9) Packaging Information

Bulk : 500pcs per bag